Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

B.Tech in Electronics and Communication with Specialization in IOT

V - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
							Theory			Practical			
				L	T	P	End Sem Unversity Exam	Two Term Exam	Teachers Asses-ment*	End Sem Unversity Exam	Teachers Asses-ment*		
1	BTEC501	Microprocessors and Interfacing		3	1	2	60	20	20	30	20	150	5
2	BTCS405	Database Management System		3	1	2	60	20	20	30	20	150	5
3	BTECIOT501	Communication Systems		3	1	2	60	20	20	30	20	150	5
4	BTEE503	Control System Engineering		3	1	0	60	20	20	0	0	100	4
5	BTEC504/ BTCS701	Elective-I: 1 CMOS VLSI Design 2 Cloud Computing		3	1	2	60	20	20	30	20	150	5
6	BTECIOT502	Embedded Systems - Raspberry pi and Arduino		0	0	2	0	0	0	30	20	50	1
7	BTIT406	Unix and Shell Programming		0	0	2	0	0	0	30	20	50	1
		TOTAL		15	5	12	300	100	100	180	120	800	26

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Paticipation in Class, given that no component shall exceed more than 10 marks.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore

Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore